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Complete if Known

Application Number	Not Yet Assigned
Filing Date	December 22, 2005
First Named Inventor	Axel Andersch
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	2003P08945WOUS

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	3	LISA GUERRA, JOACHIM FITZNER, DIPANKAR TALUKDAR, CHRIS SCHLÄGER, BASSAM TABBARA and VOJIN ZIVOJNOVIC, "Cycle and Phase Accurate DSP Modeling and Integration for HW/SW Co-Verification, Design Automation Conference, 1999 Proceedings, 36 th New Orleans, LA, USA, June 21-25, 1999, Piscataway, NJ, IEEE, June 21, 1999, Pages 964-969, XP010344032.	
	4	P. LE MARREC, C.A. VALDERRAMA, F. HESSEL and A.A. JERRYAYA, "Hardware, Software and Mechanical Cosimulation for Automotive Applications", Rapid System Prototyping, 1998, Proceedings, 1998 Ninth International Workshop on Leuven, Belgium, June 3-5, 1998, Los Alamito, CA, USA, IEEE Comp. Soc, June 3, 1998, Pages 202-206, XP010283205.	

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